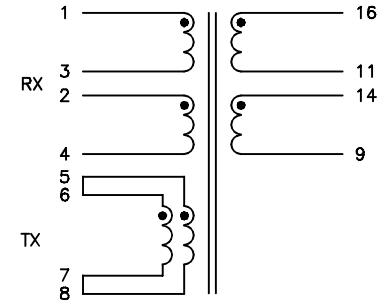
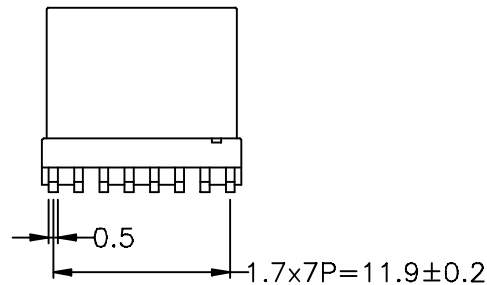
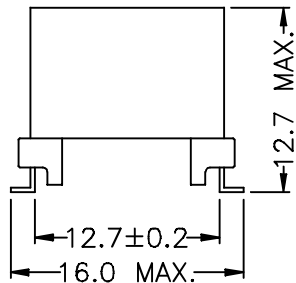


SUGGESTED P.CPB LAYOUT



\* RoHS COMPLIANCE.

ELECTRICAL CHARACTERISTICS :

1. DCR : PIN 1-4 (PIN 2-3 SHORT) = 4.4Ω MAX.  
PIN 5-8 (PIN 5-6 & 7-8 SHORT) = 0.3Ω MAX.  
PIN 16-9 (PIN 11-14 SHORT) = 3.0Ω MAX.
2. INDUCTANCE (@10kHz, 100mVrms) :  
PIN 16-9 (11-14 SHORT) : = 400uH ±10%.
3. LEAKAGE INDUCTANCE (@100kHz, 100mVrms) :  
PIN 16-9 (11-14,5-6,7-8 SHORT) = 2.5uH MAX.  
PIN 16-9 (11-14,1-2,3-4 SHORT) = 2.1uH MAX.
4. T.H.D. (@30kHz, 3Vrms) :  
PIN 16-9 (11-14,2-3,6-7, SHORT,  
1-4 W/H 100Ω, 5-8 W/H 5.6Ω) = -85dB MAX.
5. TURN RATIO (@100kHz, 100mVrms) :  
PIN 16-9 (11-14 SHORT) : 1-4 (2-3 SHORT) = 1 ±2%  
PIN 16-9 (11-14 SHORT) : 5-8 (5-6,7-8 SHORT) = 4.25 ±2%
6. HI-POT : (@1875VAC, 1mA, 2S) PRI-SEC

								TITLE VDSL TRANSFORMER	
								DWG. NO. ATS-1584R	
RELEASE						UNITS:		SAFETY	
NO: DESCRIPTION DATE BY CHK APPD						DATE		P/N:	
REVISIONS								SHEET 1 of 1 DRAW	